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For Immediate Release

**Cypress Warp™ Software Adds Support
for Programmable Serial Interface (PSI™) Devices**

***Warp Release 6.1 Simplifies Communications Line Card Development,
Adds Unix® Support for Delta39K on Sun's Solaris™ 8 OS***

SAN JOSE, Calif., May 16, 2001 – Cypress Semiconductor (NYSE: CY) today announced that the latest release of its popular Warp™ software design tools now include support for its recently-announced Programmable Serial Interface (PSI™) family of communications devices. Warp Release 6.1 also extends its Unix® support to include Cypress's Delta39K™ complex programmable logic devices (CPLDs) on Sun's Solaris™ 8 operating system.

Warp Release 6.1 now supports Cypress's full range of programmable logic offerings, from simple PLDs to the high-density Delta39K family. With support for the PSI family, Warp Release 6.1 offers a comprehensive design, development, synthesis, and simulation environment for integrated SERDES and programmable logic chip solutions. Warp software provides a seamless programming interface for designers to easily integrate custom IP with the SERDES via VHDL or Verilog blocks, text or graphical state machines.

"Warp Release 6.1 allows designers to take advantage of the advanced features of PSI devices," said Trevor Davis, Cypress's software product manager. "As a result, Warp Release 6.1 significantly simplifies communications line card development by reducing the complexity of board-level simulation and chip-to-chip communication simulation."

About Warp

Warp software is the No. 1 VHDL and Verilog-based PLD software tool, with over 27,000 installed seats. Introduced in 1991, Warp pioneered the use of HDL for programmable logic design. Its design tools accept design entries using VHDL IEEE Standard 1076/1164 or Verilog IEEE Standard 1364.

Warp works in an integrated environment with all major third-party EDA toolsets. It provides the capabilities of tools costing much more, enabling designers to capitalize on the performance and speed-to-market advantages of programmable logic with a minimal software investment.

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About PSI

Cypress's PSI devices combine the flexibility, predictable timing and ease-of-use of the company's CPLDs with their highly-successful SERDES technology, communications memory and phase-locked loops (PLLs). The PSI family is compatible with various physical layer transmission media – fiberoptic modules, copper cables and circuit board traces. The PSI family of products are designed for communications backplanes and line cards in a broad range of markets, including InfiniBand, Gigabit Ethernet, Fibre Channel, and SONET.

Pricing and Availability

Cypress continues to offer a value-driven \$99 edition of *Warp* Release 6.1, along with two editions that provide additional design functionality. In addition, Cypress offers an industry-leading value by providing customers with free technical support and upgrades for life. *Warp* Release 6.1 is available now.

About Cypress

Cypress Semiconductor is "Driving the Communications Revolution"™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include high-speed data communications ICs; networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology solutions; and controllers for Universal Serial Bus (USB).

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,700 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide Web site at <http://www.cypress.com>.

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Warp and “Driving the Communications Revolution” are trademarks of Cypress Semiconductor.